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STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	09989762
Filing Date	2001-11-19
First Named Inventor	Kenneth Y. Ogami
Art Unit	2123
Examiner Name	Osborne, Luke R.
Attorney Docket Number	CD01175M

U.S.PATENTS

Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	4176258	B1	1979-11-27	Jackson	Entire Document
	2	6094730	B1	2000-07-25	Lopez et al.	Entire Document
	3	6107826	B1	2000-08-22	Young et al.	Entire Document
	4	6161199	B1	2000-12-12	Szeto et al.	Entire Document
	5	6173419	B1	2001-01-09	Barnett	Entire Document
	6	6185522	B1	2001-02-06	Bakker	Entire Document
	7	6542844	B1	2003-04-01	Hanna	Entire Document
	8	6223272	B1	2001-04-24	Coelho et al.	Entire Document

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9	5127103	B1	1992-06-30	Hill et al.	Entire Document
10	6374370	B1	2002-04-16	Bockhaus et al.	Entire Document
11	5321828	B1	1994-06-14	Phillips et al.	Entire Document
12	5325512	B1	1994-06-28	Takahashi	Entire Document
13	5331571	B1	1994-07-19	Aronoff et al.	Entire Document
14	5357626	B1	1994-10-18	Johnson et al.	Entire Document
15	5493723	B1	1996-02-20	Beck et al.	Entire Document
16	5546562	B1	1996-08-13	Patel	Entire Document
17	5559996	B1	1996-09-24	Fujioka et al.	Entire Document
18	5572665	B1	1996-10-05	Nakabayashi et al.	Entire Document
19	5574892	B1	1996-11-12	Christensen	Entire Document

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20	5587957	B1	1996-12-24	Kowalczyk et al.	Entire Document
21	5590354	B1	1996-12-31	Klapproth et al.	Entire Document
22	5630052	B1	1997-05-13	Shah	Entire Document
23	5630102	B1	1997-05-13	Johnson et al.	Entire Document
24	5663900	B1	1997-09-02	Bhandari et al.	Entire Document
25	5691898	B1	1997-11-25	Rosenberg et al.	Entire Document
26	5911059	B1	1999-06-08	Profit, Jr.	Entire Document
27	5752013	B1	1998-05-12	Christensen et al.	Entire Document
28	5802290	B1	1998-09-01	Casselman	Entire Document
29	5805792	B1	1998-09-08	Swoboda et al	Entire Document
30	5889988	B1	1999-03-30	Held	Entire Document

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31	5984893	B1	1999-10-12	Circello et al.	Entire Document
32	6016563	B1	2000-01-18	Fleisher	Entire Document
33	5999725	B1	1999-12-07	Barbier et al.	Entire Document
34	6009270	B1	1999-12-28	Mann	Entire Document
35	6016554	B1	2000-01-18	Skrovan et al.	Entire Document
36	6034538	B1	2000-03-07	Abramovici	Entire Document
37	6058263	B1	2000-05-02	Voth	Entire Document
38	6075941	B1	2000-06-13	Itoh et al.	Entire Document
39	6289300	B1	2001-09-11	Brannick et al.	Entire Document
40	6298320	B1	2001-10-02	Buckmaster et al.	Entire Document
41	6032268	B1	2000-02-29	Swoboda et al.	Entire Document

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42	6347395	B1	2002-02-12	Payne et al.	Entire Document
43	6366878	B1	2002-04-02	Grunert	Entire Document

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U.S.PATENT APPLICATION PUBLICATIONS

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	1	20030149961	A1	2003-08-07	Kawai et al.	Entire Document
	2	20030056071	A1	2003-03-20	Triece et al.	Entire Document
	3	20020052729	A1	2002-05-02	Kyung et al.	Entire Document
	4	20020116168	A1	2002-08-22	Kim, Mahn Bok	Entire Document
	5	20020156885	A1	2002-10-24	Thakkar, Bina Kunal	Entire Document
	6	20020174134	A1	2002-11-21	Goykhman	Entire Document

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FOREIGN PATENT DOCUMENTS

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	1	BURSKY, "FPGA Combines Multiple Interfaces and Logic," Electronic Design, Vol. 48 No. 20, pp.74-78 (2 October 2000); 5 pages	<input type="checkbox"/>
	2	ANONYMOUS, "Warp Nine Engineering - The IEEE 1284 Experts-F/Port Product Sheet," undated web page found at http://www.fapo.com/fport.htm ; 2 pages	<input type="checkbox"/>
	3	ANONYMOUS, "F/Port: Fast Parallel Port for the PC" Installation Manual" Release 7.1, circa 1997, available for download from http://www.fapo.com/fport.htm ; 25 pages	<input type="checkbox"/>
	4	Nam et al.; "Fast Development of Source-Level Debugging System Using Hardware Emulation"; IEEE 2000; 4 pages	<input type="checkbox"/>
	5	Huang et al.; "Iceberg: An Embedded In-Circuit Emulator Synthesizer For Microcontrollers"; ACM 1999; 6 pages	<input type="checkbox"/>
	6	Khan et al.; "FPGA Architectures for Asic Hardware Emulators"; IEEE 1993; 5 pages	<input type="checkbox"/>
	7	Oh et al.; Emulator Environment Based on an FPGA Prototyping Board"; IEEE 21-23; June 2000; 6 pages	<input type="checkbox"/>

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8	Hong et al.; "An FPGA-Based Hardware Emulator for Fast Fault Emulation"; IEEE 1997; 4 pages	<input type="checkbox"/>
9	Ching et al.; "An In-Circuit-Emulator for TMS320C25"; IEEE 1994; 6 pages	<input type="checkbox"/>
10	Pasternak; "In-Circuit-Emulation in ASIC Architecture Cor Designs"; IEEE 1989; 4 pages	<input type="checkbox"/>
11	Melear; "Using Background Modes for Testing, Debugging and Emulation of Microcontrollers"; IEEE 1997; 8 pages	<input type="checkbox"/>
12	WALTERS, STEPHEN; "Computer-Aided Prototyping for ASIC-Based Systems", 1991, IEEE Design & Test of Computers; Volume 8, Issue 2; 8 pages	<input type="checkbox"/>
13	Anonymous; "JEEN JTAG Embedded Ice Ethernet Interface"; August 1999; Embedded Performance, Inc.; 3 pages	<input type="checkbox"/>
14	Sedory; "A Guide to Debug"; 2004; retrieved on May 20, 2005; 7 pages	<input type="checkbox"/>
15	"Microsoft Files Summary Judgement Motions"; February 1999; Microsoft PressPass; retrieved on May 20, 2005 from http://www.microsoft.com/presspass/press/1999/feb99/Feb99/Calderapr.asp ; 3 pages	<input type="checkbox"/>
16	Xerox; "Mesa Debugger Documentation"; April 1979; Xerox Systems Development Department; Version 5.0; 33 pages	<input type="checkbox"/>
17	Stallman et al.; "Debugging with the GNU Source-Level Debugger"; January 1994; retrieved on May 2, 2005 from http://www.cs.utah.edu ; 4 pages	<input type="checkbox"/>
18	Wikipedia.org; "Von Neumann architecture"; retrieved from http://en.wikipedia.org/wiki/Von_Neumann_architecture on January 22, 2007; 4 pages	<input type="checkbox"/>

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19	Stan Augarten; "The Chip Collection - Introduction - Smithsonian Institute"; "State of the Art"; "The First 256-Bit Static RAM"; retrieved on November 14, 2005 from http://smithsonianchips.si.edu/augarten/p24.htm ; 2 pages	<input type="checkbox"/>
20	"POD - Piece of Data, Plain Old Documentation, Plain Old Dos..."; retrieved on November 14, 2005 from http://www.auditmypc.com/acronym/POD.asp ; 2 pages	<input type="checkbox"/>
21	USPTO Final Rejection for 09/989,765 (CD01194M) dated 03/31/09; 18 pages	<input type="checkbox"/>
22	USPTO Final Rejection for 09/989,778 (CD01172) dated 03/16/09; 26 pages	<input type="checkbox"/>

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